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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page __1__ of __2__ PATENT NO. : 6,917,093 APPLICATION NO.: 10/691,843 ISSUE DATE 1.07/12/2005 INVENTOR(S) Chen et al. It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below: In the specification: Please replace paragraph [0022] with the following amended paragraph: Also as shown in Figure 2, photoresist 400 is applied and a STI mask is used to pattern the regions on the wafer surface where a STI trench will be formed. The trench-pattern puts the trench-edges along the <100> direction so that after trench etch. The significance of the orientation will be explained in a later paragraph. In the drawings: See attached set of Replacement formal drawings (2 sheets) with corrections to Figure 3.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Texas Instruments Incorporated P. O. Box 655474 MS 3999 Dallas, TX 75265

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